## Shift Register SN74HC595.pdf

**SER** What is to be stored in the register. Triggers the "shift" of bits to store the new bit read in by **SER**.

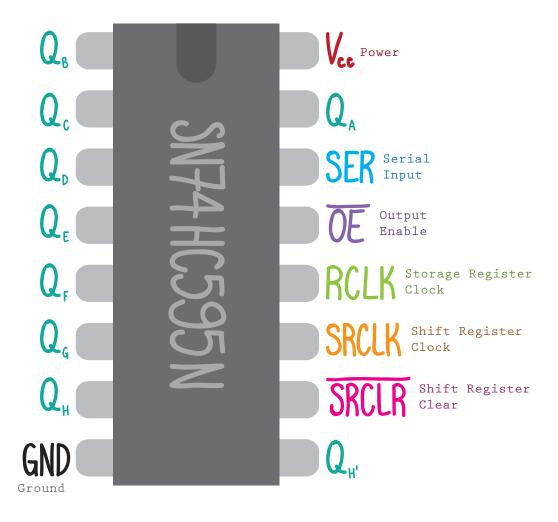
**OE** A control input that permits the output of data from the device when LOW, and prevents out put from being 'visible' when HIGH

**RCLK** Moves all data in the shift-register to the storage/output register

**SRCLK** Triggers bits to move right in register

**SRCLR** Clears all input - resets register when set to LOW

**Register** "A set of N contiguous binary cells, each of which is capable of saving the value of a single binary digit. An 8-bit register stores the values for 8 binary digits or 1 byte."



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