

Shift Register SN74HC595N

Official Datasheet at: sparkfun.com/datasheets/IC/SN74HC595.pdf

SER What is to be stored in the register. Triggers the "shift" of bits to store the new bit read in by **SER**.

\overline{OE} A control input that permits the output of data from the device when **LOW**, and prevents output from being 'visible' when **HIGH**

RCLK Moves all data in the shift-register to the storage/output register

SRCLK Triggers bits to move right in register

\overline{SRCLR} Clears all input - resets register when set to **LOW**

Register "A set of N contiguous binary cells, each of which is capable of saving the value of a single binary digit. An 8-bit register stores the values for 8 binary digits or 1 byte."

