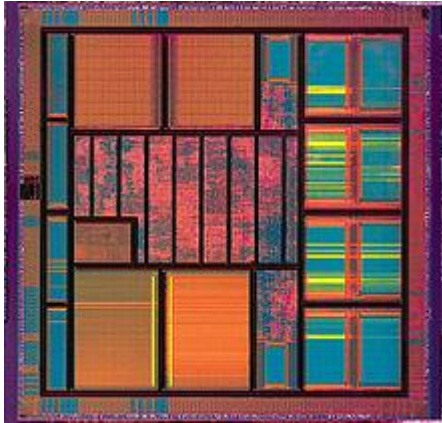New class!
EE94/194: Intro to VLSI CAD Algorithms
Comp50/150 (*crosslist*)
Spring 2017

For full details, see https://www.eecs.tufts.edu/~joelg/CAD.pdf or e-mail joelg@eecs.tufts.edu

Do chips really have so many colors inside?

We'll learn how a high-level design gets validated (both simulation and formal validation). We'll learn how designs get synthesized into gates and how those gates get checked to see if they will run at speed.

We'll learn what happens after a design is done and silicon is returned… how critical speed paths are debugged in silicon, and how manufacturing tests get generated.

Best of all – no CAD tools to run!

We'll be learning the inner life and algorithms of CAD, and why tools work the way they do. But no long hours in the lab trying to get the darn tools to work!

Most of the algorithms we learn will be very applicable to other problems in several other fields

There will be a guest speaker from Intel's Massachusetts Design Center talking about post-silicon debug at Intel.

There will be a guest speaker from Synopsys' Massachusetts R&D center talking about the CAD industry

Meet people involved in an actual $100M lawsuit over CAD algorithms. Really!